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## APPLICANTS

Hiroshi Miyagi, Yokohama, JAPAN;

\*\* CONTINUING DATA *yes* *no*

This application is a 371 of PCT/JP03/08227 06/27/2003

\*\* FOREIGN APPLICATIONS *yes* *no*

JAPAN 2002-198928 07/08/2002 NO

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged  <i>[Signature]</i> <i>[Initials]</i>	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance  Examiner's Signature <i>[Signature]</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 6
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## TITLE

Semiconductor integrated circuit and semiconductor integrated circuit manufacturing method

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